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U.S. UTILITY Patent Application

233

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| <b>**CONTINUING DATA VERIFIED:</b><br>NONE   |                           |   |                             |  |                    |
| <b>** FOREIGN APPLICATIONS VERIFIED:</b><br>JAPAN 2001-140622 05/11/2001   |                           |   |                             |  |                    |
| PG-PUB   |                           | DO NOT PUBLISH <input type="checkbox"/> |                             | RESCIND <input type="checkbox"/>                     |                    |
| Foreign priority claimed<br>35 USC 119 conditions met<br>Verified and Acknowledged Examiners's initials                    |                           |   |                             | ATTORNEY DOCKET NO<br>XA-9635                        |                    |
| TITLE : Manufacturing method of semiconductor device   |                           |   |                             | U.S. DEPT. OF COMM. PAT. & TM. PTO-4361 (Rev. 12-94) |                    |

|   |           |                           |                         |
|---|-----------|---------------------------|-------------------------|
| <b>NOTICE OF ALLOWANCE MAILED</b>   |           | <b>CLAIMS ALLOWED</b>     |                         |
|   |           | Total Claims              | Print Claim for<br>O.G. |
| <b>ISSUE FEE</b>  |           | <b>DRAWING</b>            |                         |
| Amount Due  | Date Paid | Sheets Drawn              | Figs. Drawn             |
|   |           | Print Fig.                |                         |
| <input type="checkbox"/> <b>TERMINAL<br/>DISCLAIMER</b>   |           | Application Examiner      |                         |
|   |           | <b>PREPARED FOR ISSUE</b> |                         |
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